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Chapter 1 Introduction to VLSI Testing

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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented

by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

Top 17 VLSI Interview Questions & Answers

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Bethlehem, Pa. man questions President Trump at town hall ...

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