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1. VHDL means. Very high speed integrated circuit hardware description language.
2. List the types of HDL?
3. Software used in VHDL lab is?
4. Name any two programming tools of VHDL?
5. FPGA means. Field programmable gate array.
6. CPLD

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means. Complex programmable logic design. 7. ASIC means.

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## **vhdl lab viva questions with answers**

Question 1. What Is Vhdl? Answer : VHDL

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stands for "VHSIC Hardware Description Language." VHSIC, in turn, stands for "Very High Speed Integrated Circuit," which was a U.S. Department of Defense program. Question 2. What Can Be The Various Uses Of Vhdl? Answer : The VHDL language can be used for several goals like - To synthesize digital circuits.

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These questions are very useful as FPGA viva questions also. Question -1: Write a simple VHDL program for D Flipflop and D latch. Answer -1: Refer D Flipflop VHDL Code and D Latch VHDL Code Question -2: Write a VHDL program for 4X1 MUX(Multiplexer). Answer -2: Refer 4X1 MUX VHDL Program . Question -3: Write a VHDL code for 8 to 3 encoder.

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2. Write HDL code to control speed, direction of DC and Stepper motor. 3. Write HDL code to accept 8 channel Analog signal, Temperature sensors and display the data on LCD panel or Seven segment display. 4. Write HDL code to generate different waveforms (Sine, Square, Triangle, Ramp etc.,) using DAC change the frequency and amplitude. 5.

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