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Book Summary: The title of this book is CMOS VLSI Design and it was written by Neil Weste, David Harris. This particular edition is in a Hardcover format. This books publish date is Mar 11, 2010 and it has a suggested retail price of \$179.99. It was published by Pearson and has a total of 880 pages in the book.

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CMOS VLSI Design - pages.hmc.edu

Description. The extensively revised 3rd edition of CMOS VLSI Design details modern techniques for the design of complex and high performance CMOS Systems-on-Chip. The authors draw upon extensive industry and classroom experience to explain modern practices of chip design.

Weste & Harris, CMOS VLSI Design: A Circuits and Systems ...

Solutions for CMOS VLSI Design 4th Edition. Last updated 26 March 2010. Chapter 1 1.1 Starting with 100,000,000 transistors in 2004 and doubling every 26 months for 12 years gives transistors. 1.2 See Figure 1.4 for data through 2009. Some data includes: Table 1: Microprocessor transistor counts Date CPU Transistors (millions)

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Prerequisites: CS/EE3700 (Digital Design) or equivalent is required. CS3810 (Computer Architecture) equivalent is extremely helpful. Textbook: Principles of CMOS VLSI Design: A Circuit and Systems Perspective (4th Edition) , By Neil Weste, David Harris, Published by Addison-Wesley, c2010, ISBN 978-0321547743.

CS/EE 5710/6710 Digital VLSI

Textbook: CMOS VLSI Design by Weste and Harris, 4th edition, Addison-Wesley/Pearson, 2011. PREREQUISITES: Logic design, computer architecture. Students are expected to be able to design logic circuits and implement state machines using logic and memory elements, and have an understanding of computer architecture.

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